

# The Role of Solderability Testing and Its Importance in Successful Soldering

#### By Reid Henry, President, Hentec Industries/RPS Automation

#### Abstract

A question that is often asked is what is solderability testing and what function does it perform. Solderability testing is not component preconditioning but is a measure of the ease with which a solder joint can be formed. Solderability testing is used to assess the solderability of device package terminations and varies depending on the type of solder alloy used. For example, when using various lead-free alloys, solderability can differ significantly from solderability when using lead-based solder.

**Key Terms**: Semiconductor device lifecycle, component solderability analysis, wetting balance solderability method, dip-and-look solderability method, steam aging, accelerated life testing, long-term storage conditions, end-of-life product builds

#### Background

In 1983 the Department of Defense (DoD) determined that over 40% of military electronics system field failures were electrical in nature and some 50% of these failures were due to poor solder connections. Plated finishes, usually nickel (Ni) or tin (Sn), were found to be porous and non-intermetallic allowing oxide penetration to the base metal which led to poor solder joint integrity and therefore resulted in field failures. As a result, the MIL-STD-883 solderability testing standard was instituted to assure that all components in high-reliability applications were indeed solderable and plated finishes gave way in favor of a hot solder dip finish which provides a fused tin-lead (SnPb) homogeneous surface finish to enhance overall solderability.

#### **Solderability Test Methods**

The MIL-STD-883 Method 2003 is one of the oldest and most widely used standards for solderability testing within the electronics assembly industry. It is used as the basis for the following discussion to provide details on how solderability testing is normally performed.

The MIL-STD-883 standard employs the dip-and-look solderability method, requiring the following equipment: 1) a solder pot of sufficient volume that can maintain solder at a specified temperature, 2) steam aging equipment for aging of the samples prior to testing, 3) a dipping mechanism capable of controlling the rates of immersion and exit speed, as well as dwell time, of the terminations, and 4) an optical inspection and lighting system that facilitates a minimum of at least 10X magnification.

### Why Test Solderability?

Solderability testing provides a means of determining the solderability of device package terminations that are intended to be joined to another solderable surface using either tin-lead (SnPb) or lead-free solder alloys. The reason for performing solderability testing is to detemine if a component provides



the degree of wetting necessary to form a robust solder connection. A poor solderability test result indicates that a less-than-optimal connection could be formed. Solderability testing is also effective during various aspects of printed circuit board production, such as when evaluating different printed circuit board coatings, solder alloys, solder paste, or flux materials.

In addition to today's consumer driven electronics marketplace, there are many industries that require semiconductor devices to have a longer lifecycle such as the industrial, automotive, medical, aerospace and defense sectors which dictates long-term component storage. Whenever long-term storage is required it is critical to ensure electronic components are reliable with a key measure of quality being solderability testing.

## **Testing Protocols**

Solderability testing is designed to determine how well molten solder will wet on solderable surfaces. Most common categories of solderability testing are qualitative testing and quantitative testing. The most common qualitative testing method is the dip-and-look method which involves subjective examination via comparative analysis of test samples. The most common quantitative testing method is the wetting balance method that requires interpretation of a wetting balance curve.

The wetting balance test is used to measure the wetting forces between molten solder and the surfaces of a test specimen as a function of time. Solderable surfaces differ in speed and strength of adhesion therefore the wetting balance test measures the time for various solder alloys to wet a components terminations or pads.

The dip-and-look test is a method in which the test specimen is completely immersed in molten solder and then visually inspected. A test piece is dipped into a molten solder bath, usually by an operator. The surfaces to be wetted are first wetted with flux and then completely immersed in the molten solder followed by visual examination.



Figure 1. Wetting balance solderability test system (left), and dip-and-look solderability test system (right)

The most prevalent suppliers of dip-and-look solderability test equipment are Cemco (UK) and Hentec/RPS (USA), while the major wetting balance test equipment suppliers include GEN3 Systems (UK), Malcom/Seika (Japan), Metronelec/Quiptech (Germany), Microtronic (Germany) and RHESCN (Japan) along with other minor suppliers.

In addition to aging of samples prior to solderability testing, steam aging equipment can also be used for accelerated life testing to simulate elongated storage conditions of surface mount or through-hole components. If additional testing is required for mission critical applications, X-ray fluorescence



(XRF) can be used to measure the solder thickness of the component terminations and scanning electron microscopy (SEM) can be employed to probe the intermetallic layer if necessary.

### Acceptance Criterion

The main acceptable criterion for solderability testing as defined by the MIL-STD-883 Method 2003 standard is 95% coverage of the dipped portion of the terminations with a new and continuous solder coating. Therefore, pinholes, voids, porosity, non-wetting, or de-wetting must not exceed 5% of the total dipped area of the terminations.



Figure 2. Solderability specimen exhibiting porosity (left), and test specimen displaying non-wetting (right)

Besides the MIL-STD-883 Method 2003 standard, other common solderability test standards include the IPC J-STD-002 standard. Other solderability test standards include MIL-STD-202 Method 208, J-STD-001 Space Addendum, J-STD-001ES, J-STD-003, JEDEC JESD22-B102 and MIL-STD-750 Method 2026.

#### **Qualitative vs. Quantitative**

Solderability testing determines how well molten solder will wet on solderable surfaces of electronic components with the most common solderability test methods being the dip-and-look method and the wetting balance method. The dip-and-look method is a qualitative type test performed by comparative analysis after specimens are dipped in a bath of flux and molten solder. The wetting balance method is a quantitative type test based upon the interpretation of a wetting curve measuring the buoyancy of a specimen using a load cell. There are several solderability test standards, but the most common standards are MIL-STD-883 Method 2003, IPC J-STD-002 and MIL-STD-202 Method 208.

While the wetting balance test method is precise and measures the wetting forces between molten solder and a test specimen as a function of time, it requires the interpretation of a wetting curve by skilled personnel in a laboratory environment. Another disadvantage is that wetting curves can be easily distorted if the system is not properly calibrated or if a wetting balance test is performed incorrectly by unskilled personnel.





Figure 3. Wetting balance solderability test method (left), and wetting curve of highly solderable lead (right)

An advantage of the dip-and-look method is since it is based on comparative analysis it can be performed rapidly by shop floor personnel with minimal training as well as requiring significantly lower capital investment then a wetting balance test system which is considerably more expensive.



Figure 4: Dip-and-look solderability test method, (left), and dip-and-look specimens at 10X magnification (right)

The Hentec/RPS Pulsar dip-and-look test system can also be configured for low-volume lead tinning of component terminations that exhibit poor solderability due to oxidation or prolonged storage.



Figure 5: Dip-and-look solderability test equipment (left), and component steam aging system (right)

For some high-reliability applications additional solderability testing may be required and can include steam aging which is used for accelerated life testing to simulate elongated storage conditions. The



Hentec/RPS Photon steam aging system is designed to generate artificial aging simulating elongated storage conditions of electronic components and is especially suited for high-reliability applications or end-of-life product builds.

### **Solderability Test Procedure**

The general solderability test procedure as noted by the MIL-STD-883 Method 2003 standard consists of the following steps: 1) proper preparation of the samples as defined by an acquisition document, but which must not include wiping, cleaning, scraping, or abrasive cleaning of the terminations to be tested, 2) aging of the samples in a steam ager, which consists of exposing the surfaces to be tested to water vapor for eight (8) hours and drying them either by baking at 100°C for no more than one (1) hour in a dry atmosphere or air drying them at ambient temperature for a minimum of fifteen (15) minutes; 3) proper application of flux to the terminations; 4) solder dipping, which consists of immersing the terminations in a static solder bath at a uniform temperature of 245C  $\pm$ 5°C; followed by 5) examination of the terminations at 10-15X magnification.

#### Summary

Solderability testing is essential to ensure that robust and reliable solder joints are formed for most electronic packaging and circuit board assembly applications. Solderability should no longer be consider as an option for many high reliability segments of the global electronics assembly industry, particularly whenever long-term component storage is involved. Therefor to ensure product quality solderability testing is highly recommended.

#### References

"Component Reliability After Long Term Storage," Texas Instruments, May 2008.

"Handling, Storage and Shelf Life of Semiconductor Devices," Allegro Microsystems, September 2019.

"The Effects of Long-Term Storage on Solderability of Semiconductor Components," Rochester Electronics, January 2022.

Henry, Reid, "An Engineer's Guide to Component Re-Conditioning Using the Robotic Hot Solder Dip Process," Hentec Industries/RPS Automation, April 2021.

Henry, Reid, "Techniques for Selective Soldering High Thermal Mass and Fine-Pitch Components, "Hentec Industries//RPS Automation, May 2021.

#### Hentec Industries/RPS Automation

25517 East Kildea Road Newman Lake, WA USA 99025 Email: <u>rhenry@rpsautomation.com</u> Phone: (509) 891-1680